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an output stage coupled to an output of said first stage; 10

wherein the output of the first stage is coupled to an input of the output

12 stage;

wherein the differential bus and the differential data bus are precharged to a 13 14

voltage Vpr between Vdd and ground, where Vpr = K*Vdd, and K is a precharging voltage factor.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,

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